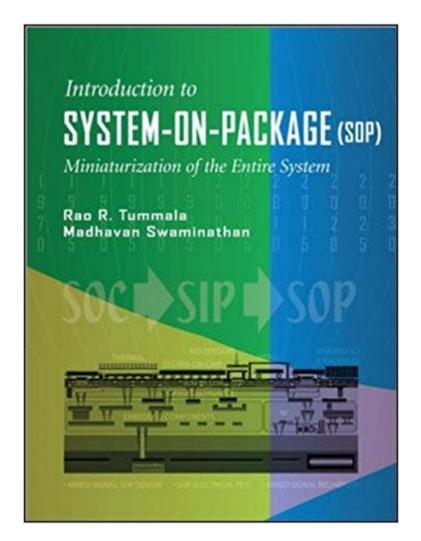


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System On Package: Miniaturization Of The Entire System





Synopsis

System-on-Package (SOP) is an emerging microelectronic technology that places an entire system on a single chip-size package. Where $\tilde{A}\phi\hat{a} \neg \mathring{A}$ "systems $\tilde{A}\phi\hat{a} \neg \mathring{A}$ • used to be bulky boxes housing hundreds of components, SOP saves interconnection time and heat generation by keep a full system with computing, communications, and consumer functions all in a single chip. Written by the Georgia Tech developers of the technology, this book explains the basic parameters, design functions, and manufacturing issues, showing electronic designers how this radical new packaging technology can be used to solve pressing electronics design challenges.

Book Information

Hardcover: 785 pages Publisher: McGraw-Hill Education; 1 edition (May 6, 2008) Language: English ISBN-10: 0071459065 ISBN-13: 978-0071459068 Product Dimensions: 7.7 x 1.4 x 9.5 inches Shipping Weight: 3.2 pounds (View shipping rates and policies) Average Customer Review: Be the first to review this item Best Sellers Rank: #787,311 in Books (See Top 100 in Books) #50 inà Â Books > Engineering & Transportation > Engineering > Electrical & Electronics > Optoelectronics #133 inà Books > Engineering & Transportation > Engineering > Electrical & Electronics > Electronics > Electronics > Semiconductors #239 inà Â Books > Engineering & Transportation > Engineering > Electrical & Electronics > Electronics > Microelectronics

Customer Reviews

Dr. Rao Tummala (Atlanta, GA) is Pettit Chair Professor and Director of the Packaging Research Center at Georgia Tech. He is a past-President of ISHM, an IEEE Fellow, and the General Chair of IEEE-ICTC. He was recently named by Industry Week as one of the 50 Stars in the US for improving national competitiveness.

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